CLAIMS

1. A display apparatus including a current driving light emitting element and a driving transistor, the display apparatus comprising:

a first switching transistor, provided between (i) a current control terminal of the driving transistor and (ii) a current output terminal of the driving transistor;

a first capacitor, connected to the current control terminal of the driving transistor; and

a second capacitor, having a first terminal and a second terminal, the first terminal being connected to the current control terminal of the driving transistor, the second terminal being connected to (i) the current output terminal of the driving transistor via a second switching transistor, and (ii) a predetermined voltage line via a third switching transistor.

2. A display apparatus including a current driving light emitting element and a driving transistor, the display apparatus comprising:

a first switching transistor, provided between (i) a current control terminal of the driving transistor and (ii) a current input terminal of the driving transistor;

a first capacitor, connected to the current control terminal of the driving transistor; and

a second capacitor, having a first terminal and a second terminal, the first terminal being connected to the current control terminal of the driving transistor, the second terminal being connected to (i) the current input terminal of the driving transistor via a second switching transistor, and (ii) a predetermined voltage line via a third switching transistor.

3. The display apparatus as set forth in claim 1 or 2, wherein:

the first capacitor, the second capacitor, the first switching transistor, the second switching transistor, and the third switching transistor are provided in each pixel circuit or each source driver circuit.

4. The display apparatus as set forth in claim 3, wherein:

each of the source driver circuits includes the first capacitor, the second capacitor, the first switching transistor, the second switching transistor, and the third switching transistor; and

each of the pixel circuits includes a transistor for controlling a current that is to be supplied to the current driving light emitting element.

5. The display apparatus as set forth in claim 1 or 2, wherein:

one or more of the first capacitor, the second capacitor, the first switching transistor, the second switching transistor, and the third switching transistor are provided in a pixel circuit, and the others are provided in a portion outside the pixel circuit, which portion includes a source driver circuit.

6. The display apparatus as set forth in claim 5, wherein:

the current driving light emitting element, the driving transistor, and the first capacitor are provided in the pixel circuit; and

the second capacitor, the first switching transistor, the second switching transistor, and the third switching transistor are provided in the portion outside the pixel circuit, which portion includes the source driver circuit,

the display apparatus, further comprising:

a connecting wire for connecting the current control terminal of the driving transistor to the first terminal of the second capacitor.

7. The display apparatus as set forth in claim 6, wherein:

the current driving light emitting element, the driving transistor, and the first capacitor are provided in the pixel circuit;

the second capacitor, the first switching transistor are provided outside the pixel circuit; and

the second switching transistor and the third switching transistor are provided in the source driver,

the display apparatus, further comprising:

a connecting wire for connecting the second terminal of the second capacitor to the second switching transistor and the third switching transistor.

8. The display apparatus as set forth in claim 5, wherein:

the current driving light emitting element, the driving transistor, the first switching transistor, the first capacitor, and the second capacitor are provided in the pixel circuit;

the second switching transistor and the third switching transistor are provided in the portion outside the pixel circuit, which portion includes the source driver circuit.

the display apparatus, further comprising:

a connecting wire for connecting the second terminal of the second capacitor to (i) the current output

terminal of the driving transistor, or (ii) the current input terminal of the driving transistor.

9. The display apparatus as set forth in claim 6 or 8, further comprising:

an OFF potential line for supplying an OFF potential;

wherein:

the connecting wire is connected to the OFF potential line via a fourth switching transistor.

10. A method for driving a display apparatus including a current driving light emitting element and a driving transistor, the method comprising the steps of:

electrically connecting a current control terminal of the driving transistor to a first terminal of a first capacitor;

electrically connecting, during a current writing period of the driving transistor, the first terminal of the first capacitor to a first terminal of a second capacitor;

during a first period, (i) electrically connecting a second terminal of the second capacitor to a predetermined voltage line, and (ii) electrically connecting the current control terminal of the driving transistor to a current output terminal of the driving transistor, and (iii)

causing the first capacitor and the second capacitor to retain a current control terminal potential that the driving transistor has on this occasion;

during a second period, (i) correcting the current control terminal potential by disconnecting the current control terminal of the driving transistor from the current output terminal of the driving transistor, and by changing electric connection of the second terminal of the second capacitor from the predetermined voltage line to the current output terminal of the driving transistor, and (ii) causing, during the second period, the first capacitor to retain the current control terminal potential that the driving transistor has on this occasion; and

controlling, during a current readout period of the driving transistor, an output current of the driving transistor with the use of the current control terminal potential, retained by the first capacitor, of the driving transistor.

11. A method for driving a display apparatus including a current driving light emitting element and a driving transistor, the method comprising the steps of:

electrically connecting a current control terminal of the driving transistor to a first terminal of a first capacitor; electrically connecting, during a current writing period of the driving transistor, the first terminal of the first capacitor to a first terminal of a second capacitor;

during a first period, (i) electrically connecting a second terminal of the second capacitor to a predetermined voltage line, and (ii) electrically connecting the current control terminal of the driving transistor to a current input terminal of the driving transistor, and (iii) causing the first capacitor and the second capacitor to retain a current control terminal potential that the driving transistor has on this occasion;

during a second period, (i) correcting the current control terminal potential by disconnecting the current control terminal of the driving transistor from the current input terminal of the driving transistor, and by changing electric connection of the second terminal of the second capacitor from the predetermined voltage line to the current input terminal of the driving transistor, and (ii) causing the first capacitor to retain the current control terminal potential that the driving transistor has on this occasion; and

controlling, during a current readout period of the driving transistor, an input current of the driving transistor with the use of the current control terminal potential, retained by the first capacitor, of the driving

transistor.

12. The driving method as set forth in claim 10 or 11, wherein:

during the second period, the electric connecting of the second terminal of the second capacitor to the current output terminal of the driving transistor is carried out before disconnecting the predetermined voltage line from the second terminal of the second capacitor.